

Appl. No. 10/687,407
Amdt. Dated January 18, 2006
Reply to Office action of October 18, 2005

REMARKS/ARGUMENTS

This amendment is being submitted in response to the Office Action dated October 18 2005.

The drawings were objected to under 37 CFR 1.83(a) and 37 CFR 1.83(b). Claims 1-15 were rejected under 35 USC §102 (b) as being anticipated by submitted prior art as represented by Applicants' Figures 1 and 2 and corresponding description. Claims 1-15 remain pending in this application. Reconsideration in view of the above amendments and following remarks is respectfully requested.

Objections to Drawings

The drawings were objected to under 37 CFR 1.83 (a) for failing to show every feature of the invention specified in the claims. The drawings were objected to under 37 CFR 1.83 (b) as being incomplete. Applicants respectfully submit that the drawings, which were actually submitted with the application, do show every feature and are complete.

Incorrect drawings appear to have been scanned by the USPTO mail processing department. These incorrect drawings have headers that make it appear that the scanner took drawings from a reference submitted as part of the IDS (Application No. 10/457322, Attorney docket No. 126335) and scanned them as part of the official record for the present application.

To expedite examination, the originally filed drawings are attached hereto. These are not replacement sheets but are the sheets actually submitted with the filing of the application. Accordingly, Applicants respectfully request that the objection of the drawings under 37 CFR 1.83 (a) and 37 CFR 1.83 (b) be withdrawn.

Claims allowable over the submitted art

Claims 1-15 were rejected under 35 USC §102 (b) as being anticipated by the submitted prior art. The present invention, as claimed in independent claim 1 is patentable over the submitted prior art. "Anticipation requires the disclosure in a single prior art reference of each element of the claim under consideration." W.L. Gore & Associates v. Garlock, Inc., 220 USPQ 303, 313 (Fed. Cir. 1983). The submitted prior art does not disclose each and every element of the present invention as claimed in independent claim 1. Claim 1 discloses an imager comprising a plurality of pixels disposed on a substrate in an imaging array pattern comprising rows and columns, each of the pixels comprising a respective photosensor coupled to a respective thin film

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switching transistor. The imager further comprises a plurality of scan lines disposed at a first level with respect to the substrate along a first axis of the imaging array pattern. Each row of pixels in the imaging array pattern having a respective scan line and each of the respective scan lines is coupled to a respective gate electrode in the thin film switching transistor for each pixel disposed along the respective row of pixels in the imaging array pattern. The imager further comprises a plurality of data lines disposed at a second level with respect to the substrate along a second axis of the imaging array pattern. Each column of pixels in the imaging array pattern having a corresponding data line and each of the respective data lines is coupled to a respective source electrode in the thin film switching transistors for each pixel disposed along the respective column of pixels in the imaging array pattern. Each pixel comprises a storage capacitor coupled parallel to the photosensor. The storage capacitor comprises a signal electrode and a capacitor common electrode and a dielectric disposed between the signal electrode and the capacitor common electrode.

The submitted prior art does not teach or disclose each and every element of amended independent claim 1. Specifically, the submitted prior art does not teach, suggest or disclose the use of a storage capacitor (paragraph 10). The storage capacitor, as recited in claim 1 of Applicants' invention, is coupled in parallel to the photosensor and configured to increase the dynamic range of the imager. More specifically, the submitted prior art does not teach, suggest or disclose the storage capacitor comprising a signal electrode and a capacitor common electrode. The submitted prior merely discusses an imager comprising several pixels, each pixel including a photosensor and a thin film transistor. However, the pixel discussed in the submitted prior art does not include a storage capacitor. Nowhere does submitted prior art teach, suggest or disclose the use of a storage capacitor in the imager.

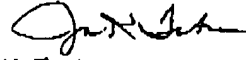
Therefore, the present invention as claimed in independent claim 1 is not anticipated by the submitted prior art. Claims 2-15 depend directly or indirectly from claim 1. Accordingly, Applicants submit that claims 2-15 are allowable by dependency. Thus, it is respectfully requested that the rejection of claims 1-15 under 35 USC §102 (b) be withdrawn.

In view of the foregoing amendment and for the reasons set out above, Applicants respectfully submit that the application is in condition for allowance. Favorable reconsideration and prompt allowance of the application are respectfully requested.

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Should the Examiner believe that anything further is needed to place the application in condition for allowance, the Examiner is requested to contact Applicants' undersigned representative at the telephone number below.

Respectfully submitted,



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Attachment: Drawing sheets 1-3